

**ABSTRACT OF THE DISCLOSURE**

5       An apparatus may include at least a first transistor, a second transistor, and a  
circuit. The first transistor has a first control terminal coupled to receive a first dynamic  
data signal, and is coupled to a first node. The first transistor drives a first state on the  
first node responsive to an assertion of the first dynamic data signal. The second  
transistor is coupled to the first node and has a second control terminal. The second  
transistor is drives a second state on the first node responsive to a signal on the second  
10   control terminal. The circuit is coupled to generate the signal on the second control  
terminal and is coupled to receive a second dynamic data signal. The second dynamic  
data signal is a complement of the first dynamic data signal, wherein the circuit is  
activates the second transistor responsive to an assertion of the second dynamic data  
signal.